



Atty. Dkt. No. 029471-0194

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Takeshi INUO

Title: A RECONFIGURABLE ELECTRIC COMPUTER,
SEMICONDUCTOR INTEGRATED CIRCUIT, AND
CONTROL METHOD, PROGRAM GENERATION METHOD,
AND PROGRAM FOR CONTROLLING RECONFIGURABLE
HARDWARE (amended)

Appl. No.: 10/561,941

371(c) Date: 2/15/2006

Examiner: Keith E. Vicary

Art Unit: 2183

Confirmation Number: 3034

AMENDMENT AND REPLY UNDER 37 CFR 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

This communication is responsive to the Final Office Action dated
September 24, 2008, concerning the above-referenced patent application.

Amendments to the Claims are reflected in the listing of claims which begins on
page 2 of this document.

Remarks/Arguments begin on page 12 of this document.

Please amend the application as follows:

02/25/2009 JAD001 00000012 10561941
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